



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/653,149
Filing Date August 31, 2000
Inventor Garo J. Derderian, et al.
Assignee Micron Technology, Inc.
Group Art Unit 2818
Examiner T. Lee
Attorney's Docket No. MI22-1330
Title: Capacitor Fabrication Methods and Capacitor

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)
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References - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed with the filing of a Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37

C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 02 Apr 2003 By: James E. Lake
James E. Lake
Reg. No. 44,854